

Notice of References Cited	Application/Control No. 09/987,039	Applicant(s)/Patent Under Reexamination TAKATA, TOSHIO	
	Examiner C.DAS	Art Unit 2122	Page 1 of 1

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*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-6,658,651	12-2003	O'Brien et al.	717/127
*	B	US-6,698,017	02-2004	Adamovits et al.	717/168
*	C	US-6,477,642	11-2002	Lupo, Van Anthony	713/2
*	D	US-5,854,913	12-1998	Goetz et al.	712/210
*	E	US-5,875,464	02-1999	Kirk, David Brian	711/129
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	M	US-			

FOREIGN PATENT DOCUMENTS

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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	TITLE: The Reduction of Branch Instruction Execution Overhead Using Structured Control Flow, author: Wedig et al, IEEE, 1984.
	V	TITLE: Instruction Fetch Mechanisms for Multipath Execution Processors, author: Klauser et al, IEEE, 1999.
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	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
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